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VS**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. 09/602,395
Filing Date June 22, 2000
Inventor John T. Moore
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Pham, T.
Attorney's Docket No. MI22-1384
Title: Methods of Forming Oxide Regions Over Semiconductor Substrates

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

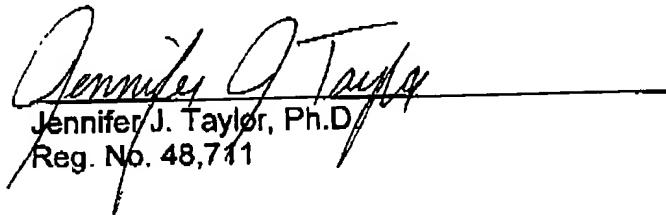
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Citation of these references is respectfully requested.

JAN 15 2003

TECHNOLOGY CENTER 2800

Respectfully submitted,

Date: 1-15-2002
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711